Application Number: 10620045 Filing Date: 15-Jul-2003 Title of Invention: Method and apparatus for automatic generation of multiple integrated circuit simulation configuration First Named Inventor/Applicant Name: Rejarshi Bhettacharya Filer: Joseph Bernard Ryan Attorney Docket Number: 1-4-2-2-1 Filed as Large Entity Utility Filing Fees Description Fee Code Quantity Amount Sub-Total i USD(\$) Basic Filling: Pages: Claims: Miscellaneous-Filing: Petition: Patent-Appeals-and-Interference: Filing a brief in support of an appeal 1402 1 500 500 Post-Allowance-and-Post-Issuance:	Electronic Paten	t App	olication Fe	e Transr	nittal					
Method and apparatus for automatic generation of multiple integrated circuit simulation configuration First Named Inventor/Applicant Name: Rajarshi Bhattacharya Filer: Joseph Bernard Ryan Attorney Docket Number: 1-4-2-2-1 Filed as Large Entity Utility Filing Fees Description Fee Code Quantity Amount Sub-Total I USD(s) Basic Filing: Pages: Claims: Miscellaneous-Filing: Petition: Patent-Appeals-and-Interference: Filing a brief in support of an appeal 1402 1 500 500	Application Number:	10	10620045							
circuit simulation configuration First Named Inventor/Applicant Name: Rajarshi Bhattacharya Filer: Joseph Bernard Ryan Attorney Docket Number: 1-4-2-2-1 Filed as Large Entity Utility Filing Fees Description Fee Code Quantity Amount Sub-Total I USD(\$) Basic Filing: Pages: Claims: Miscellaneous-Filing: Petition: Patent-Appeals-and-Interference: Filing a brief in support of an appeal 1402 1 500 500	Filing Date:	15	-Jul-2003							
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